

IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

First Named Inventor: Brian Robert Prasky

Date: January 16, 2008

USSN 10/796.426

Filed March 9, 2004

Title: Method, System And Program Product For A Pipelined Processor Having A Branch Target Buffer (BTB) To Create A Recent Entry Queue In Parallel With The Branch Target Buffer (BTB) (formerly titled: Effective delayed, minimized switching, BTB write via recent entry queue that has the ability to delay decode):

Confirmation: 1895

Attorney Docket POU920030068US1:

Art Unit 2183 Examiner Brian P. Johnson

Deposit Account: 09-0463 :

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RCE AMENDMENT in Response to Office Action of 10/16/2007

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

Introductory Comments:

Applicants respectfully request entry of the following amendment for the RCE of the original application. (The Commissioner is also hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 09-0463.)